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PAGE 1/15 : RCVD AT 4/30/2004 5:11:21 PM [Eastern Daylight Time] : SVR:USPTO-EFAX-2/1 : DNS:7469195 : CSID:206.342.6201 : DURATION (mm:ss):05-16



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Fax Transmission | April 30, 2004

TO: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

FROM: Mark R. Hennings

OUR REF: 50019.0256US01
TELEPHONE: 206.342.6200

Total pages, including cover letter: 15

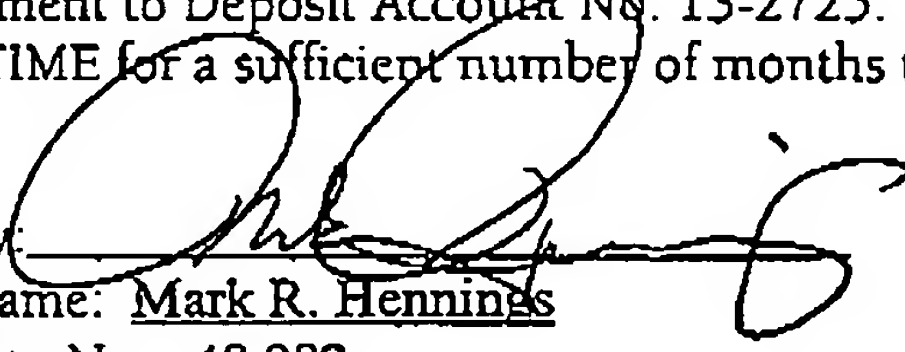
PTO FAX NUMBER 1-703-746-9195

If you do NOT receive all of the pages, please telephone us at 206.342.6200, or fax us at 206.342.6201.


Title of Document Transmitted: AREA EFFICIENT ON-CHIP TIMEOUT
GENERATOR WITH LOW TEMPERATURE AND
LOW SUPPLY VOLTAGE DEPENDENCY

Applicant: Schouten, et al.
Serial No.: 10/650,594
Filed: 08/27/2003
Group Art Unit: Not Yet Assigned
Our Ref. No.: 50019.0256US01
Confirmation No. 7925

Please charge any additional fees or credit overpayment to Deposit Account No. 13-2725. Please consider this a PETITION FOR EXTENSION OF TIME for a sufficient number of months to enter these papers, if appropriate.

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Name: Mark R. Hennings
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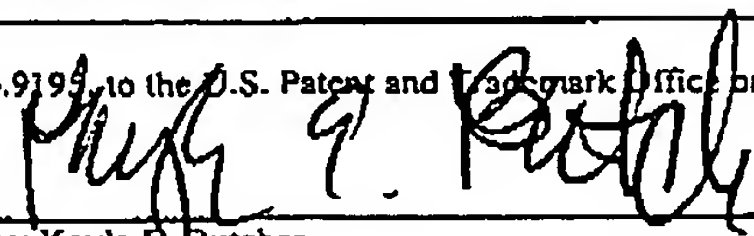


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Schouten, et al. Examiner: Not Yet Assigned
Serial No.: 10/650,594 Group Art Unit: Not Yet Assigned
Filed: 08/27/2003 Docket: 50019.0256US01
Title: AREA EFFICIENT ON-CHIP TIMEOUT GENERATOR WITH LOW
TEMPERATURE AND LOW SUPPLY VOLTAGE DEPENDENCY

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By: 
Name: Kayla E. Butcher

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PATENT TRADEMARK OFFICE

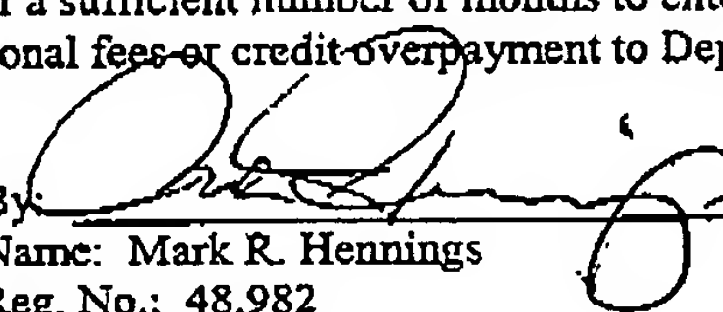
Sir:

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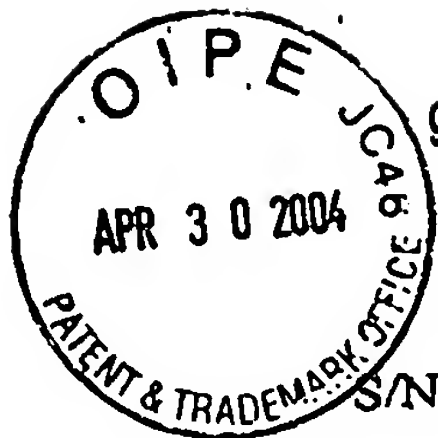
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- ☒ Request for Corrected Filing Receipt
- ☒ Copy of Original Filing Receipt
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- ☒ Assignment of the invention to National Semiconductor Corporation., Recordation Form Cover Sheet
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P.O. Box 2903, Minneapolis, MN 55402-0903
612.332.5300

By: 
Name: Mark R. Hennings
Reg. No.: 48,982
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S/N 10/650,594

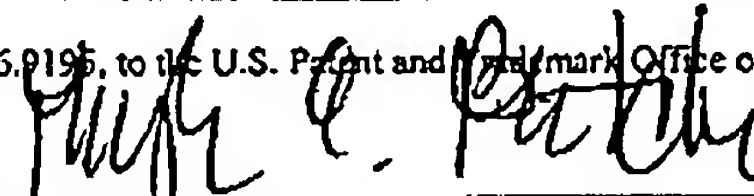
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Schouten, et al.	Examiner:	Not Yet Assigned
Serial No.:	10/650,594	Group Art Unit:	Not Yet Assigned
Filed:	08/27/2003	Docket No.:	50019.0256US01
Title:	AREA EFFICIENT ON-CHIP TIMEOUT GENERATOR WITH LOW TEMPERATURE AND LOW SUPPLY VOLTAGE DEPENDENCY		

CERTIFICATE UNDER 37 CFR 1.6(d):

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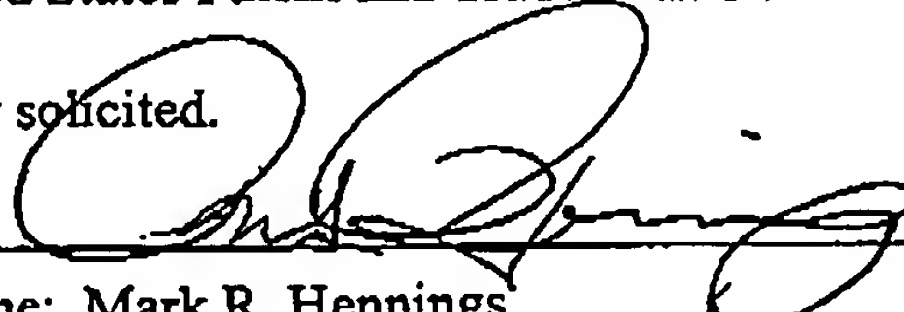
Dear Sir:

Enclosed is a photocopy of the filing receipt from the United States Patent and Trademark Office in the above-identified application showing requested corrections. The filing receipt is erroneous in the following respects as reflected in the papers originally filed: 08/27/2003.

The papers were filed with the USPTO without formal signature on the Declaration and Power of Attorney as well as the Assignment. We attach herewith the executed Power of Attorney and Assignment for recordation with the USPTO.

Correction of the records of the United States Patent and Trademark Office and issuance of a corrected filing receipt are respectfully solicited.

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Minneapolis, MN 55402-0903

By: 
Name: Mark R. Hennings
Reg. No.: 48,982



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/650,594	08/27/2003		750	✓ 50019.256US01/PO5688	3	19	3

CONFIRMATION NO. 7925

23552
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MINNEAPOLIS, MN 55402-0903

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FILING RECEIPT



OC000000012433008

Date Mailed: 04/23/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Andre Schouten, Inverkip, UNITED KINGDOM;
Stephen O'Kane, Greenock, UNITED KINGDOM;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 04/22/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

Area efficient on-chip timeout generator with low temperature and low supply voltage dependency

Preliminary Class

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